#### PRODUCT BULLETIN UPDATE

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23-May-2016 **ISSUE DATE:** 

**NOTIFICATION:** 16880A

MPC5604P ERRATA UPDATE TITLE:

**EFFECTIVE** 24-May-2016

DATE:

#### **DEVICE(S)**

| MPN              |  |
|------------------|--|
| SC667395VLQ6R    |  |
| SPC5601PEF0MLH   |  |
| SPC5601PEF0MLH6  |  |
| SPC5601PEF0MLH6R |  |
| SPC5601PEF0MLHR  |  |
| SPC5601PEF0MLL   |  |
| SPC5601PEF0MLL6  |  |
| SPC5601PEF0MLL6R |  |
| SPC5601PEF0MLLR  |  |
| SPC5601PEF0VLH   |  |
| SPC5601PEF0VLH6  |  |
| SPC5601PEF0VLH6R |  |
| SPC5601PEF0VLHR  |  |
| SPC5601PEF0VLL   |  |
| SPC5601PEF0VLL6  |  |
| SPC5601PEF0VLL6R |  |
| SPC5601PEF0VLLR  |  |
| SPC5602PEF0MLH   |  |
| SPC5602PEF0MLH6  |  |
| SPC5602PEF0MLH6R |  |
| SPC5602PEF0MLHR  |  |
| SPC5602PEF0MLL   |  |
| SPC5602PEF0MLL6  |  |
| SPC5602PEF0MLL6R |  |
| SPC5602PEF0MLLR  |  |
|                  |  |

| SPC5602PEF0VLH   |
|------------------|
| SPC5602PEF0VLH6  |
| SPC5602PEF0VLH6R |
| SPC5602PEF0VLHR  |
| SPC5602PEF0VLL   |
| SPC5602PEF0VLL4  |
| SPC5602PEF0VLL4R |
| SPC5602PEF0VLL6  |
| SPC5602PEF0VLL6R |
| SPC5602PEF0VLLR  |
| SPC5603PEF0MLL6  |
| SPC5603PEF0MLL6R |
| SPC5603PEF0VLQ6  |
| SPC5603PEF0VLQ6R |
| SPC5603PEF1MLL4  |
| SPC5603PEF1MLL4R |
| SPC5603PEF1MLL6  |
| SPC5603PEF1MLL6R |
| SPC5603PEF1VLQ6  |
| SPC5603PEF1VLQ6R |
| SPC5603PF1VLL6   |
| SPC5603PF1VLL6R  |
| SPC5603PGF0MLL4  |
| SPC5603PGF0MLL6  |
| SPC5603PGF0MLL6R |
| SPC5603PGF0MLQ6  |
| SPC5604PEF0MLL4  |
| SPC5604PEF0MLL4R |
| SPC5604PEF0MLL6  |
| SPC5604PEF0MLL6R |
| SPC5604PEF0MLQ4  |
| SPC5604PEF0MLQ4R |
| SPC5604PEF0MLQ6  |
| SPC5604PEF0MLQ6R |
| SPC5604PEF0VLL6  |
| SPC5604PEF0VLL6R |
| SPC5604PEF0VLQ6  |
| SPC5604PEF1MLL4  |
|                  |

| SPC5604PEF1MLL6  |
|------------------|
| SPC5604PEF1MLL6R |
| SPC5604PEF1MLQ4  |
| SPC5604PEF1MLQ6  |
| SPC5604PEF1MLQ6R |
| SPC5604PEF1VLL6  |
| SPC5604PEF1VLL6R |
| SPC5604PEF1VLQ6  |
| SPC5604PEF1VLQ6R |
| SPC5604PF0VLL6   |
| SPC5604PF0VLL6R  |
| SPC5604PF1VLL6   |
| SPC5604PF1VLL6R  |
| SPC5604PF1VLQ6   |
| SPC5604PF1VLQ6R  |
| SPC5604PGF0MLL4  |
| SPC5604PGF0MLL4R |
| SPC5604PGF0MLL6  |
| SPC5604PGF0MLL6R |
| SPC5604PGF0MLQ4  |
| SPC5604PGF0MLQ4R |
| SPC5604PGF0MLQ6  |
| SPC5604PGF0MLQ6R |
| SPC5604PGF0VLL4  |
| SPC5604PGF0VLL4R |
| SPC5604PGF0VLL6  |
| SPC5604PGF0VLL6R |
| SPC5604PGF1MLL6  |
| SPC5604PGF1MLL6R |
| SPC5604PGF1MLQ6  |
| SPC5604PGF1VLL6  |
| SPC5604PGF1VLL6R |
| SPC5604PGF1VLQ6  |
| SPC5604PGF1VLQ6R |

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## **AFFECTED CHANGE CATEGORIES**

ERRATA

#### **DESCRIPTION OF CHANGE**

16880A revision contains no change in the errata content for MPC5604P. This revision is to remove device Pictus 256K part number as a datasheet update from this notification. There is no change to the original notification for the customer's that order pictus512K part number.

The MPC5604P Errata have been updated to revision 28 Aug 2015. The revision history included in the updated document provides a detailed description of the changes. The MPC5604P Errata can be found at

http://cache.freescale.com/files/32bit/doc/errata/MPC5604P1M36W.pdf

Errata added as below:

e5230: ADC: "Abort command" aborts the ongoing injected channel as well as the upcoming normal channel.

e5569: ADC: the current conversion sequence will be corrupted if new conversion sequence started when the current conversion is going.

e5113: ADC: triggering an ABORT or ABORTCHAIN before the conversion start.

e3032: Auto clock off feature does not work for adclksel=1

e6620: FLASH: ECC error reporting is disabled for APC field greater than RWSC field.

e3610: FlexCAN: wrong data transmission exiting from STOP mode in case EXTAL frequency is greater than IRC.

e7322: FlexCAN: Bus Off interrupt bit is erroneously asserted when soft reset is performed while FlexCAN is in Bus Off state.

e7877: FlexPWM: do not enable the fault filter.

e7804: LINFlex: Consecutive headers received by LIN Slave trigger error interrupt.

e6082: LINFlexD: LINS bit in LIN status register are not usable in UART mode.

e7394: MC ME: incorrect mode may be entered on low power mode exit.

e6726: NPC: MCKO clock may be gated one clock period early when MCKO frequency is programmed as SYS\_CLK/8 and gating is enabled.

e4146: when an ACD conversion is injected, the aborted channel is not restored under certain condition.

e6967: eDMA: Possible misbehaviour of a preempted channel when using continuous link mode.

e6239: eTimer: When counter is not enabled, capture flag can be set but capture register values are not updated.

e6802: eTimer: Extra input capture events can set unwanted DMA request

e6583: eTimer: incorrect updating of the hold register.

A summary of the document changes is attached to this notice.

### **REASON FOR CHANGE**

The MPC5604P Errata have been updated to correct errors and provide additional technical clarification on some device features.

# ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)

There is no impact to device form, fit or reliability but Errata conditions can affect functionality.

#### NOTE:

THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a <u>Support Case</u>. Be aware that after you select this link to enter your request, you must choose the topic "Product Change Notification" once on the Salesforce page.

For sample inquiries - please go to www.nxp.com

#### **RELATED NOTIFICATION(S):**

16880- MPC5604P Errata Update

TO VIEW the GENERIC copy, click on the notification number above.

**QUALIFICATION STATUS:** N/A

#### **QUALIFICATION PLAN:**

N/A

#### **RELIABILITY DATA SUMMARY:**

N/A

## **ELECTRICAL CHARACTERISTIC SUMMARY:**

N/A

## **CHANGED PART IDENTIFICATION:**

N/A

# ATTACHMENT(S):

External attachment(s) FOR this notification can be viewed AT:

16880A MPC5604P 1M36W.pdf

16880A 15251482M 73811 1 PB16880-Delta-Qualification-Matrix-ZVEI-2 2 4.pdf