## NSR02F30NXT5G

# **Schottky Barrier Diode**

These Schottky barrier diodes are optimized for low forward voltage drop and low leakage current. The DSN2 (Dual Silicon No-lead) package is a chip level package using solderable metal contacts under the package similar to DFN style packages. The DSN style package enables 100% utilization of the package area for active silicon, offering a significant performance per board area advantage compared to products in plastic molded packages. The low thermal resistance enables designers to meet the challenging task of achieving higher efficiency and meeting reduced space requirements.

#### **Features**

- Very Low Forward Voltage Drop 370 mV @ 10 mA
- Low Reverse Current 7.0 μA @ 10 V VR
- 200 mA of Continuous Forward Current
- ESD Rating Human Body Model: Class 3B
  - Machine Model: Class C
- Very High Switching Speed
- Low Capacitance CT = 7 pF
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### **Typical Applications**

- LCD and Keypad Backlighting
- Camera Photo Flash
- Buck and Boost dc-dc Converters
- Reverse Voltage and Current Protection
- Clamping & Protection

#### Markets

- Mobile Handsets
- MP3 Players
- Digital Camera and Camcorders
- Notebook PCs & PDAs
- GPS

#### **MAXIMUM RATINGS**

Rating		Symbol	Value	Unit
Reverse Voltage		$V_R$	30	V
Forward Current	Forward Current (DC)		200	mA
Forward Surge Current (60 Hz @ 1 cycle)		I <sub>FSM</sub>	4.0	А
ESD Rating:	Human Body Model Machine Model	ESD	>8.0 >400	kV V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



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# 30 V SCHOTTKY BARRIER DIODE





#### DSN2 (0201) CASE 152AA

#### MARKING DIAGRAM

PIN 1

V230 = Specific Device Code YYY = Year Code



Q = Specific Device Code M = Month Code

#### **ORDERING INFORMATION**

Device	Package	Shipping†
NSR02F30NXT5G	DSN2 (Pb-Free)	5000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## NSR02F30NXT5G

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Min	Тур	Max	Unit
Thermal Resistance Junction-to-Ambient (Note 1) Total Power Dissipation @ T <sub>A</sub> = 25°C	R <sub>θJA</sub> P <sub>D</sub>			400 312	°C/W mW
Thermal Resistance Junction–to–Ambient (Note 2) Total Power Dissipation @ T <sub>A</sub> = 25°C	R <sub>θJA</sub> P <sub>D</sub>			170 735	°C/W mW
Storage Temperature Range	T <sub>stg</sub>			-40 to +125	°C
Junction Temperature	TJ			+125	°C

- Mounted onto a 4 in square FR-4 board 10 mm sq. 1 oz. Cu 0.06" thick single sided. Operating to steady state.
   Mounted onto a 4 in square FR-4 board 1 in sq. 1 oz. Cu 0.06" thick single sided. Operating to steady state.

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Leakage $(V_R = 10 \text{ V})$ $(V_R = 30 \text{ V})$	I <sub>R</sub>			7.0 50	μΑ
Forward Voltage (I <sub>F</sub> = 10 mA) (I <sub>F</sub> = 200 mA)	V <sub>F</sub>			0.37 0.55	V
Total Capacitance (V <sub>R</sub> = 5.0 V, f = 1 MHz)	СТ		7.0		pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## NSR02F30NXT5G

## **TYPICAL CHARACTERISTICS**

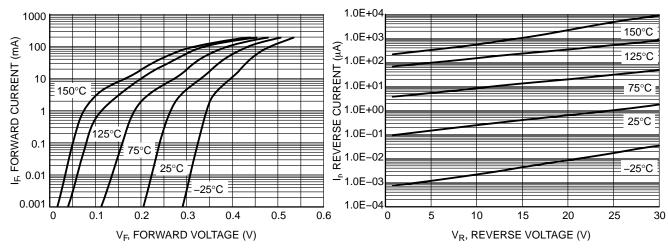


Figure 1. Forward Voltage

Figure 2. Leakage Current

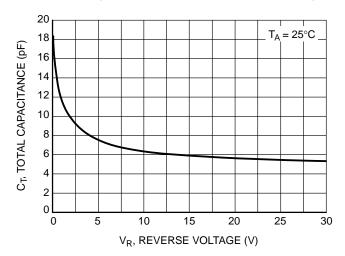


Figure 3. Total Capacitance



DSN2, 0.6x0.3, 0.4P, (0201) CASE 152AA **ISSUE B** 

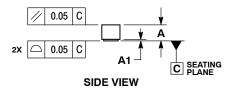
**DATE 30 APR 2017** 

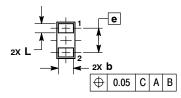
#### NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
   CONTROLLING DIMENSION: MILLIMETERS.

	MILLIMETERS		
DIM	MIN	MAX	
Α	0.24	0.30	
A1	0.00	0.01	
b	0.20	0.22	
D	0.30 BSC		
E	0.60 BSC		
е	0.40 BSC		
L	0.10	0.12	

# В 2X \alpha 0.06 2X 🗀 0.06 С **TOP VIEW**





**BOTTOM VIEW** 

#### **GENERIC MARKING DIAGRAM1\***

## PIN<sub>1</sub> XXXX YYY

## **GENERIC MARKING DIAGRAM2\*** PIN 1



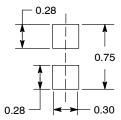
XXXX = Specific Device Code YYY = Year Code

X = Specific Device Code

M = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present. Some products may not follow the Generic Marking.

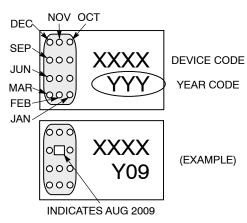
#### **MOUNTING FOOTPRINT\***



DIMENSIONS: MILLIMETERS

See Application Note AND8398/D for more mounting details

#### **CATHODE BAND MONTH CODING**



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DESCRIPTION:	DSN2, 0.6X0.3, 0.4P, (0201)		PAGE 1 OF 1	

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<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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