

# Product/Process Change Notice - PCN 14\_0060 Rev. -

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This notice is to inform you of a change that will be made to certain ADI products (see Material Report). Any issues with this PCN or requirements to qualify the change (additional data or samples) must be sent to ADI within 30 days of publication date. ADI contact information is listed below.

PCN Title: ADUM3211 Die Revision and Datasheet Change

Publication Date: 05-Mar-2014

Effectivity Date: 03-Jun-2014 (the earliest date that a customer could expect to receive changed material)

#### **Revision Description:**

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## **Description Of Change**

- 1. Increased pulse width of edge pulses and refresh pulses
- 2. Increased separation between consecutive pulses on rising edge and refresh high pulses

Datasheet changes as follows:

- 1. Increase of 0.6mA/0.1mA in IDD1/IDD2 Max Spec @ 10Mbps for 5.0V/5.0V and 5.0V/3.3V Operation
- 2. Increase of 0.5mA/0.1mA in IDD1/IDD2 Max Spec @ 10Mbps for 3.3V/3.3V and 3.3V/5.0V Operation

## **Reason For Change**

- 1. Ensure receiver can reliably detect all edge pulses and all refresh pulses
- 2. Ensure 2nd pulse on rising edge or refresh high is consistently detected by receiver

Datasheet changes:

1. Increase in size of existing capacitors in receiver block caused increase in Idd-Dynamic current

## Impact of the change (positive or negative) on fit, form, function & reliability

No change to fit, form, or reliability. Improved functionality.

## **Summary of Supporting Information**

Qualification has been performed per ADI0012, Procedure for Qualification of New or Revised Processes. See attached Qualification Report Summary.

# **Supporting Documents**

Attachment 1: Type: Qualification Report Summary

ADI\_PCN\_14\_0060\_Rev\_-\_ADuM3211 R2 Qualification.doc

#### For questions on this PCN, send email to the regional contacts below or contact your local ADI sales representative

Rest of Asia: PCN ROA@analog.com

Appendix A - Affected ADI Models						
Added Parts On This Revision - Product Family / Model Number (6)						
ADUM3211 / ADUM3211ARZ	ADUM3211 / ADUM3211ARZ-RL7	ADUM3211 / ADUM3211BRZ	ADUM3211 / ADUM3211BRZ-RL7	ADUM3211 / ADUM3211TRZ		
ADUM3211 / ADUM3211TRZ-RL7						

Appendix B - Revision History					
Rev	Publish Date	Effectivity Date	Rev Description		
Rev	05-Mar-2014	03-Jun-2014	Initial Release		

Analog Devices, Inc.

Docld:2803 Parent Docld:2672 Layout Rev:7