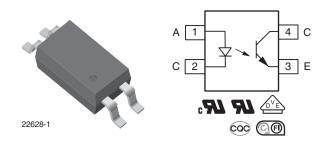
# VOS618A

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# Optocoupler, Phototransistor Output, Low Input Current, SSOP-4, Half Pitch, Mini-Flat Package



### DESCRIPTION

The VOS618A series has a GaAs infrared emitting diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a 4-pin 50 mil lead pitch mini-flat package.

It features a high current transfer ratio at low input current, low coupling capacitance, and high isolation voltage.

The coupling devices are designed for signal transmission between two electrically separated circuits.

### FEATURES

- High CTR with low input current
- Low profile package (half pitch)
- High collector emitter voltage, V<sub>CEO</sub> = 80 V
- Isolation test voltage = 3750 V<sub>RMS</sub>
- Low coupling capacitance
- High common mode transient immunity
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

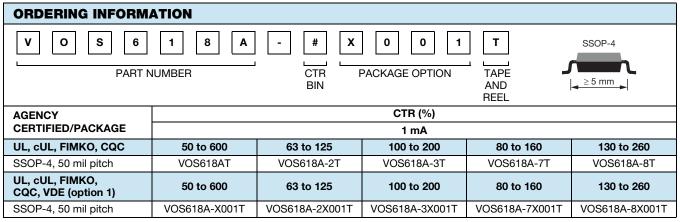
#### **APPLICATIONS**

- Telecom
- Industrial controls
- Battery powered equipment
- Office machines
- Programmable controllers

#### **AGENCY APPROVALS**

Safety application model number covering all products in this datasheet is VOS618A. This model number should be used when consulting safety agency documents.

- UL1577, file no. E52744
- cUL
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- FIMKO EN 60065, EN 60950-1
- CQC GB4943.1-2011 and GB8898-2011 (suitable for installation altitude below 2000 m)



#### Note

Additional options may be possible, please contact sales office.

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RoHS

COMPLIANT

HALOGEN

FREE

**GREEN** 

(5-2008)



<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
Reverse voltage		V <sub>R</sub>	6	V		
Power dissipation		P <sub>diss</sub>	70	mW		
Forward current		I <sub>F</sub>	50	mA		
OUTPUT						
Collector emitter voltage		V <sub>CEO</sub>	80	V		
Emitter collector voltage		V <sub>ECO</sub>	7	V		
Collector current		Ι <sub>C</sub>	50	mA		
Collector current	$t_p/T = 0.5, t_p < 10 ms$	Ι <sub>C</sub>	100	mA		
Power dissipation		P <sub>diss</sub>	150	mW		
COUPLER						
Isolation test voltage between emitter and detector	t = 1 min	V <sub>ISO</sub>	3750	V <sub>RMS</sub>		
Total power dissipation		P <sub>tot</sub>	170	mW		
Storage temperature range		T <sub>stg</sub>	-55 to +150	°C		
Ambient temperature range		T <sub>amb</sub>	-55 to +110	°C		
Junction temperature		Tj	125	°C		
Soldering temperature <sup>(1)</sup>	t = 10 s	T <sub>sld</sub>	260	°C		

#### Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

<sup>(1)</sup> Refer to reflow profile for soldering conditions for surface mounted devices.

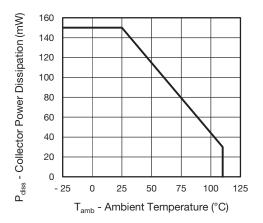


Fig. 1 - Power Dissipation vs. Ambient Temperature

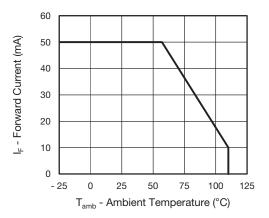


Fig. 2 - Forward Current vs. Ambient Temperature



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb}$ = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION SYMBOL		MIN.	TYP.	MAX.	UNIT	
INPUT							
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>		1.1	1.5	V	
Reverse current	V <sub>R</sub> = 6 V	I <sub>R</sub>		0.01	10	μA	
Input capacitance	$V_F = 0 V, f = 1 MHz$	CI		8		pF	
OUTPUT							
Collector emitter leakage current	V <sub>CE</sub> = 10 V	I <sub>CEO</sub>		0.7	100	nA	
Collector emitter breakdown voltage	I <sub>C</sub> = 100 μA	BV <sub>CEO</sub>	80			V	
Emitter collector breakdown voltage	I <sub>E</sub> = 10 μA	BV <sub>ECO</sub>	7			V	
Collector emitter capacitance	$V_{CE} = 5 V, f = 1 MHz$	C <sub>CE</sub>		6		pF	
COUPLER							
Collector emitter saturation voltage	I <sub>F</sub> = 1 mA, I <sub>C</sub> = 0.25 mA	V <sub>CEsat</sub>		0.12	0.4	V	
Cut-off frequency	$I_F$ = 10 mA, $V_{CC}$ = 5 V, $R_L$ = 100 $\Omega$	f <sub>ctr</sub>		119		kHz	
Coupling capacitance	f = 1 MHz	Cc		0.3		pF	

#### Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

<b>CURRENT TRANSFER RATIO</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I <sub>C</sub> /I <sub>F</sub>	I <sub>F</sub> = 1 mA, V <sub>CE</sub> = 5 V	VOS618A	CTR	50		600	%
		VOS618A-2	CTR	63		125	%
		VOS618A-3	CTR	100		200	%
		VOS618A-7	CTR	80		160	%
		VOS618A-8	CTR	130		260	%

SWITCHING CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
NON-SATURATED							
Turn on time		t <sub>on</sub>		5		μs	
Rise time	$V_{22} = 5 V_{12} = 2 m \Lambda_{12} = 100 \Omega_{12}$	t <sub>r</sub>		5		μs	
Turn off time	$V_{CC}$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$	t <sub>off</sub>		8		μs	
Fall time		t <sub>f</sub>		7		μs	
SATURATED							
Rise and fall time	$I_{F}$ = 1.6 mA, $V_{CC}$ = 5 V, $R_{L}$ = 1.9 $k\Omega$	t <sub>r</sub>		10		μs	
Fall time		t <sub>f</sub>		11		μs	
Turn on time		t <sub>on</sub>		14		μs	
Turn off time		t <sub>off</sub>		12		μs	

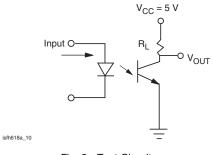


Fig. 3 - Test Circuit

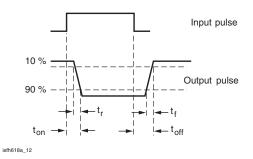


Fig. 4 - Test Circuit and Waveforms

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## **Vishay Semiconductors**

SAFETY AND INSULATION RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Climatic classification (according to IEC 68 part 1)			55/110/21			
Comparative tracking index		CTI	175			
Maximum rated withstanding isolation voltage	40 % to 80 % RH, AC test of t = 1 min	V <sub>ISO</sub>	3750	V <sub>RMS</sub>		
Maximum transient isolation voltage		V <sub>IOTM</sub>	6000	V <sub>peak</sub>		
Maximum repetitive peak isolation voltage		V <sub>IORM</sub>	565	V <sub>peak</sub>		
	$T_{amb} = 25 \text{ °C}, V_{DC} = 500 \text{ V}$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω		
Isolation resistance	$T_{amb} = 100 \ ^{\circ}C, \ V_{DC} = 500 \ V$	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω		
Output safety power		P <sub>SO</sub>	300	mW		
Input safety current		I <sub>SI</sub>	200	mA		
Input safety temperature		T <sub>SI</sub>	150	°C		
Creepage distance			≥ 5	mm		
Clearance distance			≥ 5	mm		
Insulation thickness		DTI	≥ 0.4	mm		
Environment (pollution degree in accordance to DIN VDE 0109)			2			

Note

• As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

#### TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

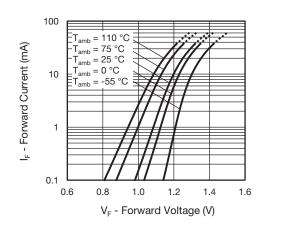


Fig. 5 - Forward Voltage vs. Forward Current

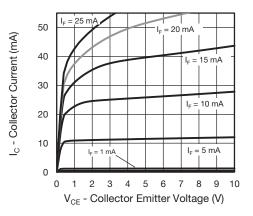
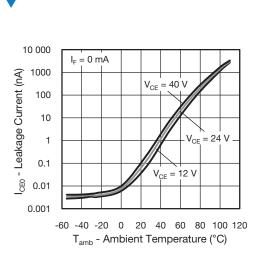


Fig. 6 - Collector Current vs. Collector Emitter Voltage



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Fig. 7 - Collector Emitter Current vs. Ambient Temperature

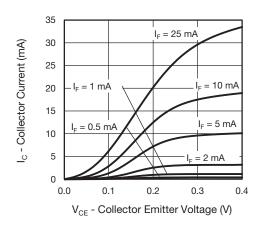


Fig. 8 - Collector Current vs. Collector Emitter Voltage

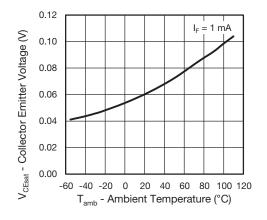


Fig. 9 - Collector Emitter Voltage vs. Ambient Temperature

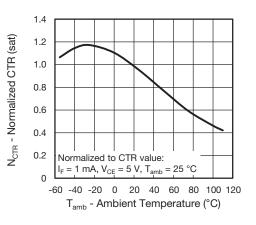


Fig. 10 - Normalized Current Transfer Ratio vs. Ambient Temperature (saturated)

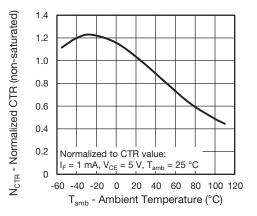


Fig. 11 - Normalized Current Transfer Ratio vs. Ambient Temperature (non-saturated)

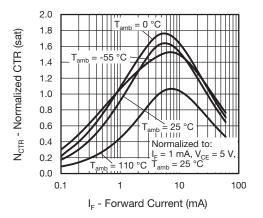
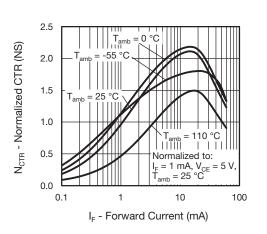


Fig. 12 - Current Transfer Ratio vs. Forward Current (saturated)

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Fig. 13 - Current Transfer Ratio vs. Forward Current (non-saturated)

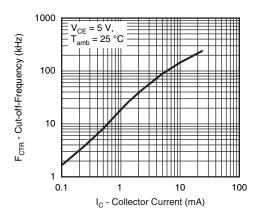


Fig. 14 - Cut-off Frequency (- 3 dB) vs. Collector Current

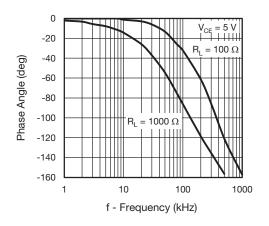


Fig. 15 - Phase Angle vs. Frequency

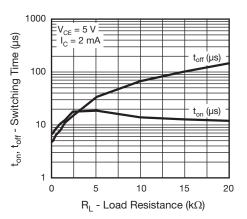


Fig. 16 - Switching Time vs. Load Resistance

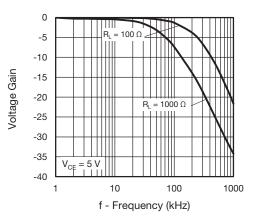
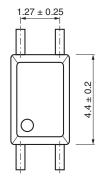


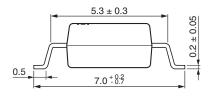
Fig. 17 - Voltage Gain vs. Frequency

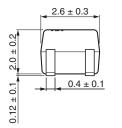
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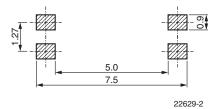


#### **PACKAGE DIMENSIONS** in millimeters

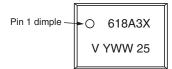








#### PACKAGE MARKING (example of VOS618A-3X001T)



#### Notes

- Option 1 is reflected with letter "X".
- Tape and reel suffix (T) is not part of the package marking.

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### TAPE AND REEL DIMENSIONS in millimeters

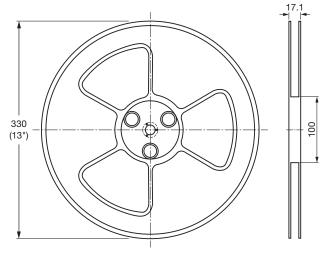


Fig. 18 - Reel Dimensions (3000 units per reel)

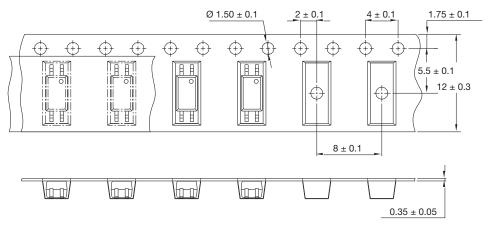


Fig. 19 - Tape Dimensions

#### SOLDER PROFILE

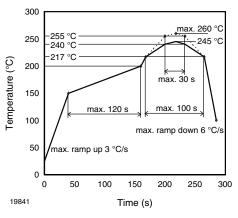


Fig. 20 - Lead (Pb)-free Reflow Solder Profile according to J-STD-020

### HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited Conditions:  $T_{amb} < 30$  °C, RH < 85 % Moisture sensitivity level 1, according to J-STD-020



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