

Product Change Notification / SYST-27KKDF218

Date:

29-Jan-2014

Product Category:

Real-Time Clock/Calendar

PCN Type:

Document Change

Notification Subject:

Data Sheet - MCP79400/01/02 Battery-Backed I2C RTCC w/ SRAM and Protected EEPROM Data Sheet Document Revision

Affected CPNs:

SYST-27KKDF218_Affected_CPN_01292014.pdf SYST-27KKDF218_Affected_CPN_01292014.csv

Notification Text:

SYST-27KKDF218

Microchip has released a new DeviceDoc for the MCP79400/01/02 Battery-Backed I2C RTCC w/ SRAM and Protected EEPROM of devices. If you are using one of these devices please read the document located at MCP79400/01/02 Battery-Backed I2C RTCC w/ SRAM and Protected EEPROM.

Notification Status: Final

Description of Change: Updated overall content for improved clarity. Added detailed description of registers. Updated block diagram and application schematic. Defined names of all bits and registers and renamed bits shown in table 7-1. Renamed the DC characteristics shown in table 7-2.

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 28 Jan 2014

NOTE: Please be advised that this is a change to the document only the product has not been changed..

Markings to Distinguish Revised from Unrevised Devices:N/A

Attachments:

MCP79400/01/02 Battery-Backed I2C RTCC w/ SRAM and Protected EEPROM

Please contact your local Microchip sales office with questions or concerns regarding this notification.

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