

Product Bulletin

Document # : PB22678Z Issue Date: 5 April 2019

Title of Change:	Update of NCV78723 datasheet.	
Effective date:	5 April 2019	
Contact information:	Contact your local ON Semiconductor Sales Office or < <u>iiri.matousek@onsemi.com</u> >	
Type of notification:	This Product Bulletin is for notification purposes only. ON Semiconductor will proceed with implementation of this change upon publication of this Product Bulletin.	
Change Category:	Wafer Fab Change Assembly Change	☐ Test Change
Change Sub-Category(s): Manufacturing Site Addition Manufacturing Site Transfer Manufacturing Process Change		✓ Datasheet/Product Doc change ☐ Shipping/Packaging/Marking ☐ Other:
Sites Affected:	ON Semiconductor Sites: None	External Foundry/Subcon Sites: None
Description and Purpose:		
Update of the datasheet at the following points: - SHORTLEDx detection flag corrected/changed to latched (pages 19, 20 and 28) - SHORTLEDx detection is not active when LEDx channel is off (page 19) - Fail Safe Operation/Stand Alone modes description improved: - FSO_MD[2:0] preloaded from OTP at power-up only (page 22) - FSO mode can be excited by writing FSO_MD[2:0] = 000 or 001 (page 23) - Registers except FSO_MD[2:0] are preloaded with 0x00h after POR in mode 2 and 3 (page 23) - "SW Compensation of the Buck Current Accuracy" equations updated (Thot according to TC_VERSION bit) (page 16) - Typos present on some places corrected: - instead of BUCK1_ and BUCK2_ only BUCKx_OC_OCCMP_THR[1:0] exists in OTP memory (page 22) - New OPN NCV78723MW2R2G added to the datasheet (page 31), Fab indicator added to marking diagram (page 1) - NCV78723MW0G OPN removed from Ordering Information table (page 31)		
List of Affected Part: Note: Only the standard (off the shelf) part numbers are listed in the parts list. Any custom parts affected by this PCN are shown in the customer specific PCN addendum in the PCN email notification, or on the PCN Customized Portal. NCV78723MW0R2G		

TEM001797 Rev. B Page 1 of 1

